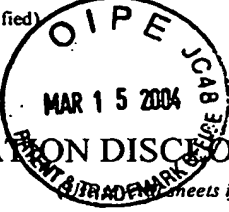


Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. INTEL/17846	Serial No. 10/734,959
 <p>INFORMATION DISCLOSURE STATEMENT</p>		Applicant Ngai et al.	
		Filing Date December 12, 2003	Group Art Unit Unknown

U.S. PATENT DOCUMENTS


*EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	INVENTOR(S)	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01					

FOREIGN PATENT DOCUMENTS

*Examiner Initials	Document Number	Publication Date	Country	Class	Subclass	Translation Yes No
	B01					

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>AL</i>	C01	Kim et al., <i>Compiling for Speculative Architectures</i> , Proceedings of the 12th International Workshop on Languages and Compilers for Parallel Computing [online] (August 1999). Retrieved from http://www.ece.purdue.edu/~eigenman/reports/lcpc99.ps .
<i>AYC</i>	C02	Zahran et al., <i>Dynamic Thread Resizing for Speculative Multithreaded Processors</i> , International Conference on Computer Design [online] (October 2003). Retrieved from http://www.enee.umd.edu/~mzahran/publications.html .
<i>AYL</i>	C03	Molina et al., <i>Trace-Level Speculative Multithreaded Architecture</i> [online] (no date). Retrieved from http://www.etse.urv.es/~cmolina/Home/Files/iccd02.pdf .

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